# Multilayer Ceramic Chip Capacitors

#### dutiliayer cerainic chip capacitor







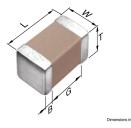
1 of 3 Creation Date : May 29, 2017 (GMT)



## C3216X7R1V106M160AC

#### **TDK item description** C3216X7R1V106MT\*\*\*\*

Applications	Commercial Grade	
Feature	General General (Up to 50V)	
Series	C3216 [EIA 1206]	
Status	Production	



Size		
Length(L)	3.20mm ±0.20mm	
Width(W)	1.60mm ±0.20mm	
Thickness(T)	1.60mm ±0.20mm	
Terminal Width(B)	0.20mm Min.	
Terminal Spacing(G)	1.00mm Min.	
Recommended Land Pattern (PA)	2.10mm to 2.50mm(Flow Soldering)	
Recommended Land Fattern (FA)	2.00mm to 2.40mm(Reflow Soldering)	
Recommended Land Pattern (PB)	1.10mm to 1.30mm(Flow Soldering)	
Recommended Land Fattern (FB)	1.00mm to 1.20mm(Reflow Soldering)	
Recommended Land Pattern (PC)	1.00mm to 1.30mm(Flow Soldering)	
Recommended Land Fattern (FC)	1.10mm to 1.60mm(Reflow Soldering)	

Electrical Characteristics		
Capacitance	10μF ±20%	
Rated Voltage	35VDC	
Temperature Characteristic	X7R(±15%)	
Dissipation Factor (Max.)	7.5%	
Insulation Resistance (Min.)	50ΜΩ	

Other		
Coldaring Mathed	Wave (Flow)	
Soldering Method	Reflow	
AEC-Q200	No	
Packing	Blister (Plastic)Taping [180mm Reel]	
Package Quantity	2000pcs	

<sup>!</sup> Images are for reference only and show exemplary products.

<sup>!</sup> This PDF document was created based on the data listed on the TDK Corporation website.

<sup>!</sup> All specifications are subject to change without notice.

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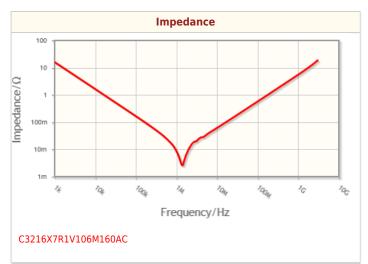


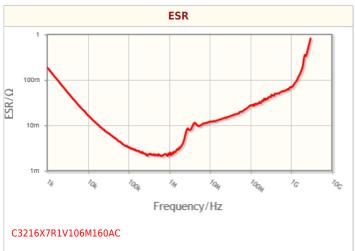


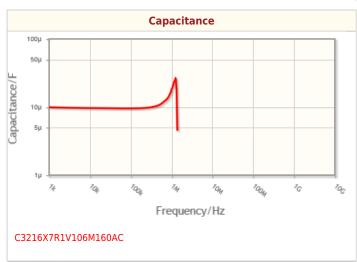


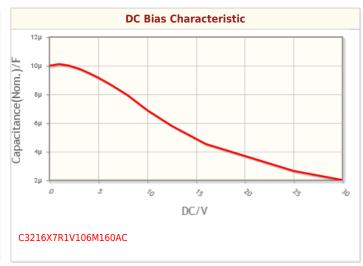


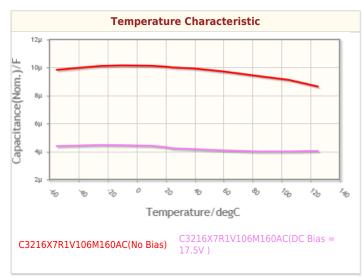
# Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

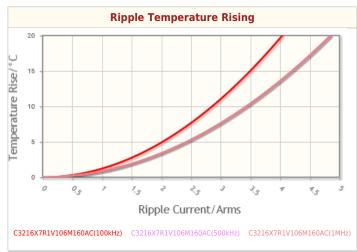












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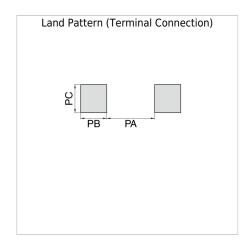








# **Associated Images**



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